Electronic Acknowledgement Receipt				
EFS ID:	5524411			
Application Number:	10697751			
International Application Number:				
Confirmation Number:	8750			
Title of Invention:	VOLTAGE-CONTROLLED DELAY CIRCUIT USING SECOND-ORDER PHASE INTERPOLATION			
First Named Inventor/Applicant Name:	Woogeun Rhee			
Customer Number:	48063			
Filer:	William Edward Lewis			
Filer Authorized By:				
Attorney Docket Number:	YOR920030258US1			
Receipt Date:	16-JUN-2009			
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1 Change of Address	YOR920030258US1_CCA.pdf	112053	no	1	
		dc848029166be4d281a45d2606572fa0f1a bd6b8			

Warnings:

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.